

**AMENDMENTS TO THE SPECIFICATION**

*Please replace the paragraph at page 10, lines 14-20 with the following amended paragraph:*

Next, searcher control logic 213 conducts search operation under the search enable signal (SEARCH\_EN) and the search offset signal (~~SERCH\_OFFSET~~SEARCH\_OFFSET, starting point of the search) of the digital signal processor 214. The DPCCH signal transmitted from the mobile station passes through the channel, and is filtered out as I channel and Q channel through a receiver filter (Rx Filter). The filtered I or Q channel signal is inputted in each decimator ~~210~~ 201 & 202, respectively, in a designated sample form (Chip\*8).

*Please replace the paragraph at page 13, lines 6-11 with the following amended paragraph:*

Referring to Figure 5, the DPCCH signal that passed through the mobile station's channel is inputted in the form of I channel and Q channel at the receiving filter (Rx Filter) (S501). The filtered I and Q channel signals are inputted in the decimator in the form of the designated sample (e.g., Chip\*8), and the decimator, following the decimation position control signal (DEC\_POS\_CTL), outputted ~~from~~ from the searcher control logic 214, decimates the sample to a certain ratio (e.g., Chip\*2) (S502).